

User's Manual

External charactor display unit type A6FD



REVISIONS

*The manual number is given on the bottom left of the back cover.

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INTRODUCTION

Thank you for choosing the Mitsubishi MELSEC-A Series of General Purpose Programmable Controllers. Please read this manual carefully so that the equipment is used to its optimum. A copy of this manual should be forwarded to the end User.

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CONTENTS

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| 1. | GENERAL DESCRIPTION $1-1 \sim 1-2$ |
|------------|---|
| 1.1 | How to Use This Manual ······1-1 |
| 2 . | SYSTEM CONFIGURATION 2-1 ~ 2-2 |
| 2.1 2.2 | System Configuration ······2-1 Equipment ······2-2 |
| | |
| 3. | SPECIFICATIONS ···································· |
| 3.1 3.2 | General Specifications ·······3-1 Performance Specifications ······3-2 |
| 3.3 | Interface Specifications 3-3 |
| 3.4 | Power Supply Specifications ···································· |
| 4. | HANDLING |
| 4.1 | Handling Instructions ···································· |
| 4.2 | Nomenclature ···································· |
| 5. | LOADING AND INSTALLATION $5-1 \sim 5-8$ |
| 5.1 | Installation Environment ······5-1 |
| 5.2 | Mounting |
| | 5.2.1 General precautions |
| | 5.2.2 Mounting |
| 5.3 | Wiring |
| | 5.3.1 Wiring precautions |
| | 5.3.2 Output unit wiring ······5-7 |
| 6. | DISPLAY CONTROL SIGNALS AND TIMING $\cdots 6-1 \sim 6-4$ |
| 6.1 | Display Output Timing ·······6-1 |
| 6.2 | Display Method ······6-4 |
| 7. | PROGRAMMING |
| 7.1 | Basic Display Programs ······7-1 |
| 7.2 | Programming Application Examples 1 (For A1, A2, A3CPU)7-6 |
| 7.3 | Programming Application Example 2 (For A0J2CPU) ······· |
| 7.4 | Display of Numerical Variables ······7-24 |
| 8. | TEST |
| 8.1 | Pre-Test Checks ······8-1 |
| 8.2 | Test and Adjustment ······8-1 |

| 9. TROUBLESHOOTING | |
|--------------------------------|----------------------------------|
| 10. MAINTENANCE AND INSPECTION | |
| APPENDICES | APP-1 ~ APP-3 |
| APPENDIX 2 Character Code List | APP-1 APP-2 put Unit APP-3 |

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1. GENERAL DESCRIPTION

This User's Manual describes the specifications, handling, programming procedures, etc. of the A6FD external display unit (hereinafter referred to as "A6FD") for use with the MELSEC-A series of Programmable.Controllers.

The examples used in this manual are based on the MELSEC-A Series PCs. Other systems may be compatible providing the A6FD specifications are met.

1.1 How to Use This Manual

This manual is divided as follows:

- Chapter 2 System Configuration Describes the system configurations required for using the A6FD.
- Chapter 3 Specifications Performance, electrical and power supply specifications.
- Chapter 4 Handling Nomenclature and handling instructions.
- Chapter 5 Loading and Installation Installation and wiring instructions
- Chapter 6 Display control signals and timing. Explains the control signals required from the output unit to the A6FD.
- Chapter 7 Programming Explains the special instructions used to control the A6FD and gives program examples that do not use the special A6FD control instructions.
- Chapter 8 Test Pre-test and test procedures
- Chapter 9 Troubleshooting
- Chapter 10 Maintenance and Inspection

1. GENERAL DESCRIPTION



Appendices

External dimensions, character set and codes. Notes on connection of the A6FD with general types of output devices.

The following manuals may also be required A1(E), A2(E), A3(E)CPU User's Manual A1(E), A2(E), A3(E)CPU Programming Manual A0J2CPU User's Manual (CPU edition) A0J2CPU Programming Manual A6GPP Operating Manual (A series) Other A series manuals



2 SYSTEM CONFIGURATION

2.1 System Configuration

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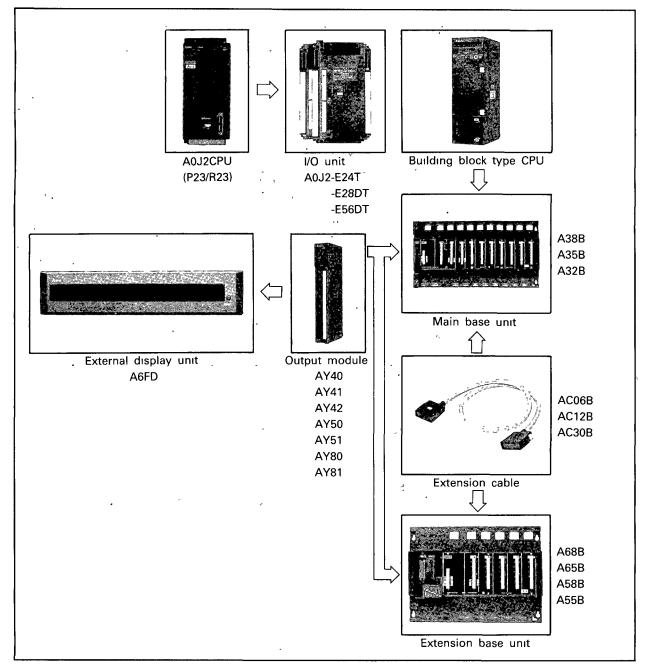


Fig. 2.1 System Configuration

| POINT | |
|--------------|---|
| stati and | sing the A6FD in a data link system remote I/O ion, refer to Section 6.1"Display Output Timing" (1) Section 7.2 "Programming Application Example" |
| A32 | extension base unit cannot be connected to the B main base unit which does not have an extension nector. |

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2.2 Equipment

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Table 2.1 lists the A series output and I/O units to which the A6FD can be connected. (available at time of printing):

| Name | Туре | Output Specifications |
|----------|--------|--|
| | 42/40 | 16-point, 12/24V DC transistor output unit |
| | AY40 | (for 0 1A) (Sink output) |
| | AY41 | 32-point, 12/24V DC transistor output unit |
| | AY41 | (for 0.1A) (Sink output) |
| Output | AY42 | 64-point, 12/24V DC transistor output unit |
| unit | AT4Z | (for 0.1A) (Sink output) |
| (for A1, | AY50 | 16-point, 12/24V DC transistor output unit |
| 2, 3CPU) | | (for 0 5A) (Sink output) |
| | AY51 | 32-point, 12/24V DC transistor output unit |
| | | (for 0 5A) (Sink output) |
| | A.V/00 | 16-point, 12/24V DC transistor output unit |
| | AY80 | (for 0.5A with fuse) (Source output) |
| | 4.1/01 | 32-point, 12/24V DC transistor output unit |
| | AY81 | (for 0 5A with fuse) (Source output) |

| Name | Туре | Output Specifications | | | |
|------------|------------|-----------------------|----------------------------------|--|--|
| 10 | A0J2-E24T | 24 points | 12/24V DC transistor output unit | | |
| I/O unit | A0J2-E28DT | 12 noints | (for 0.5A) (Sink output) | | |
| (for A0J2) | A0J2-E56DT | 24 points | (IOI 0.5A) (Sink Output) | | |

Table 2.1 Unit List

POINT

- 1. Relay output units should not be used due to the duty cycles imposed by the special A6FD control instructions
- 2. Any I/O slot may be used for the control of the A6FD.



3. SPECIFICATIONS

3.1 General Specifications

Table 3.1 shows general specifications of the A6FD.

| ltem | Specifications | | | | | |
|---------------------------------|---|-------------|--------------|-------------|--------------------|--|
| Operating ambient temperature | 0 to 55°C | | | | | |
| Storage ambient temperature | | | -20 to 7 | 5°C | | |
| Operating ambient humidity | 10 to 90%RH, non-condensation | | | | | |
| Storage ambient humidity | | 10 to | 90%RH, non- | condensatic | on | |
| | | Frequency | Acceleration | Amplitude | Sweep Count | |
| Vibration resistance | Conforms to * ¹ JIS C 0911 | 10 to 55Hz | | 0 075mm | 10 times | |
| | | 55 to 150Hz | 1g | | *(1 octave/minute) | |
| Shock resistance | Conforms to JIS C0912 (10g x 3 times in 3 directions) | | | | | |
| Noise durability | By noise simulator 1500Vpp noise voltage, 1µs noise width and 25 to 60Hz noise frequency | | | | | |
| Dielectric withstand voltage | 1500V AC for 1 minute across batch of AC external terminals and ground 500V AC for 1 minute across DC external terminals and ground | | | | | |
| Insulation resistance | $5M\Omega$ or larger by 500V DC insulation resistance tester across AC external terminals and ground | | | | | |
| - Grounding | Class 3 grounding, grounding is not necessary of the operation of the unit | | | | | |
| Operating ambience | Free of corrosive gases Dust should be minimal | | | | | |
| Cooling method | Self-cooling | | | | | |

Table 3.1 General Specifications

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REMARKS

One octave marked ***** indicates a change from the initial frequency to double or half frequency. For example, any of the changes from 10Hz to 20Hz, from 20Hz to 40Hz, from 40Hz to 20Hz, and 20Hz to 10Hz are referred to as one octave.

*¹JIS[.] Japanese Industrial Standard.



3.2 Performance Specifications

| Table | 3.2 | shows | the | A6FD | performance | specifications. |
|-------|-----|-------|-----|------|-------------|-----------------|
| | | | | | 1 | |

| ltem | Specifications | | | |
|---|--|--|--|--|
| Line voltage | Input voltage. 100V AC/200V AC (0 12A max) | | | |
| Applicable line voltage | 85 to 264V AC (50/60Hz \pm 5%) | | | |
| Applicable unit | AY40, AY41, AY42, AY50, AY51, A0J2-E24T, A0J2-E28DT, A0J2-E56DT sink output units AY80, AY81 source output units | | | |
| Interface Rated input voltage Rated input current Applicable voltage Input resistance | 8 data lines (D0 to D7), 1 strobe line (STROBE), 1 common line (COM) 12/24V DC 4mA (12V DC), 10mA (24V DC) 10 2 to 26.4V DC(Ripple ratio within 5%) Approx. 2.4KΩ | | | |
| Internal isolation | · Photocoupler | | | |
| Display type | LED (red) 5 x 7 dots, dynamic method, - 16 digits in 1 line ASCII | | | |
| Controls fitted | . Manual reset switch | | | |
| External connection | 20-point terminal connector M3 (Metric thread) \times 6 screws | | | |
| Applicable wire size | 2mm² (14AWG) or less (Applicable tightening torque 7kg cm (0.51lb ft)) | | | |
| Applicable solderless terminal | To fit 3mm (0 12 inch) dia. terminal screws 1 25-3, 1.25-YS3A, 2S3, 2-YS3A, V1 25-3, V1.25-YS3A, V2-S3, V2-YS3A | | | |
| Size mm(inch) | 145(5.71) (D) x 290(11 42) (W) x 60(2 36) (H) | | | |
| Weight kg(lb) | Approximately 1 5(3.3) | | | |

Table 3.2 Performance Specifications

The A6FD is not guaranteed against instantaneous power failure.

REMARKS

Before shipment, the A6FD is checked to ensure that it displays data properly, using a shielded cable 200m(656 18ft) in length and 0 18mm²(24AWG) CSA



3.3 Interface Specifications

| 1/0 | Internal Circuit | Terminal Signal | | A6FD ← PC Output (I/O in slot "n") | |
|--------|----------------------|-----------------|---------------------------|---|----------------------------|
| | | Number | Signal | Sink output unit | Source output unit |
| | | о тві | D0 | | Yn + 0 |
| | | ф ТВ2 | D1 | D1 ← Yn + 1 | |
| | - | ф твз | D2 | $D2 \leftarrow 1$ | |
| | | ф тв4 | D3 | D3 ← ` D4 ← ` | |
| | · | О ТВ5 | D4 | D5 ← ` | |
| Input | | О ТВ 6 | D5 | $D6 \leftarrow Yn + 6$ $D7 \leftarrow Yn + 7$ STROBE $\leftarrow Yn + 8$ Consists of 8 outputs for data signals Yn+0 to Yn+7 (D0 to D7), 1 output for strobe signal Yn+8 (STROBE), and COM (TB11). | |
| Input | | О ТВ7 | D6 | | |
| | | ф тв8 | D7 | | |
| | | о твэ | STROBE (Strobe signal) | | |
| | | ф ТВ10 | NC | | |
| | | о тв11 | COM (Common) | Connect 12/24V DC. (+) | Connect 12/24V DC. (0V) |
| | \sim | φ TB12 | NC | The reset switch clears the A6FD displa and outputs a reset signal from TB13 ar TB14 to the field. TB13 and TB14 are rela contact outputs | |
| | | Q TB13 | RESET1 | | |
| Output | Reset switch | Q TB14 | RESET2 | | |
| | ⊶ — A6FD | Q TB15 | NC | | |
| | To displayTo display | ф ТВ16 | NC | Refer to Table 3.4 | |

The external equipment interface of the A6FD are given below.

Table 3.3 External Interface Specifications

| ltem | Specifications | | |
|-------------------------------|-------------------------------|--|--|
| Applicable voltage | 100V AC, 200V AC, 5 to 48V DC | | |
| Minimum contact current value | 1mA | | |
| Maximum contact current value | 2A (Resistor load) | | |

Table 3.4 "Reset" output from A6FD

POINT

- 1. The same 12/24V DC power supply should be used for both the A6FD and the transistor output module.
- 2. The reset switch clears the display and initializes the unit. It also closes the relay contacts across TB13 and TB14.
- 3. The "NC" terminals are unused by the A6FD (NC for no connection)

REMARKS

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For the sink and source output units, refer to Appendix 3



3.4 Power Supply Specifications

The A6FD power supply specifications are given below.

| Internal Circuit | Terminal Number | Signal | Specifications |
|------------------|--------------------|-----------------|---|
| | , TB18 | 100, 200V AC | Any supply of between 85 and 264V AC may be applied to terminals TB18 and TB19. |
| | 5 TB19 | 100, 200V AC | Input current 012A (max.) Frequency. 50/60Hz ± 5%, Internal voltage: 5V DC 2A |
| | р тв20 | ĹĠ | Power filter ground |

Table 3.5 Power Supply Specifications

POINT

- 1. A 200msec delay occurs between power-up and the A6FD becoming active.
- 2. Half the supply voltage appears at the LG terminal (TB20). Do not touch this terminal if the unit is not grounded.

3-4



4. HANDLING

4.1 Handling Instructions

Handle the A6FD carefully as described below:

- (1) Do not subject the unit to impact loads.
- (2) Guard against the entry of conductive debris into the unit. If any should enter, switch off the power and remove it.
- (3) Do not touch the printed circuit board.
- (4) Tighten screws to the torques given below:

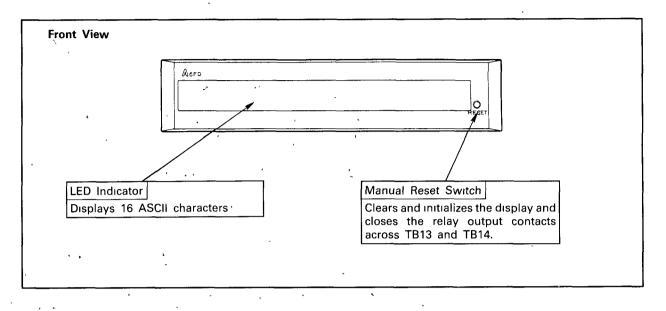
| Screw | Torque Range kg cm (lb ft) |
|---|----------------------------|
| Terminal block screws (M3 metric thread) | 8 (0 58) to 14 (1.01) |
| Terminal block fixing screws (M4 metric thread) | - 8 (0.58) to 14 (1.01) |
| Unit mounting screws (M4 metric thread) | 5 (0.36) to 8 (0.58) |

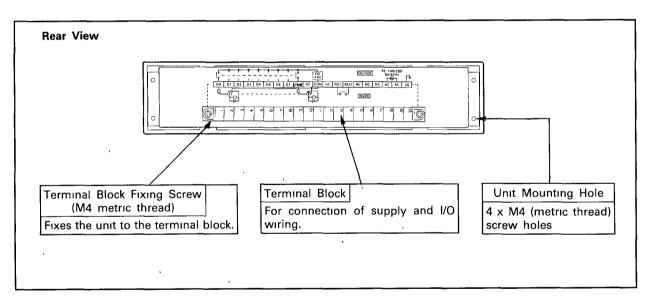
4. HANDLING

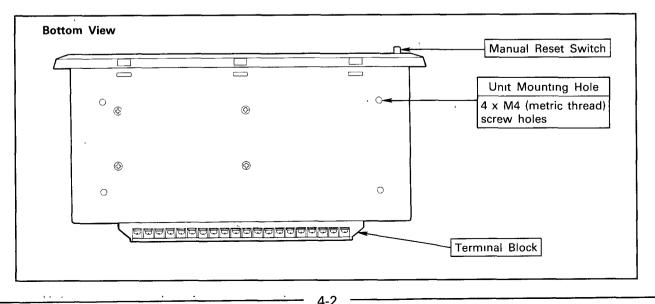


4.2 Nomenclature

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5. LOADING AND INSTALLATION

5.1 Installation Environment

The installation environment should meet the following requirements:

(1) Ambient temperature between 0 and 55°C.

(2) Ambient humidity between 10 and 90%.

(3) No condensation (e.g. due to sudden temperature changes)

(4) No corrosive and/or combustible gases.

(5) No airborne conductive or organic powders or mists.

(6) Protected from direct exposure to sunlight.

(7) Protected from strong power and magnetic fields.

(8) Protected from vibration and shock.

(9) Away from sources of heat such as heating elements.

The installation environment should meet the specifications quoted in Section 3.1.



5.2 Mounting

5.2.1 General precautions

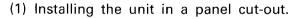
When installing the A6FD in a panel etc. note the following:

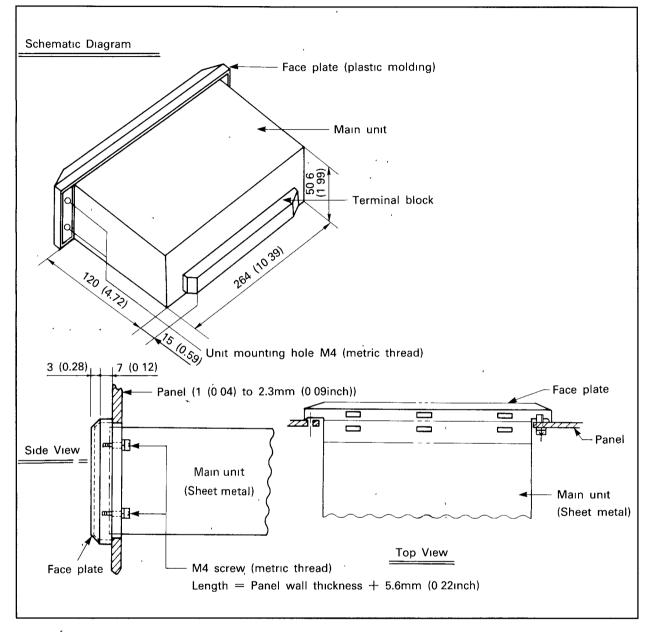
- (1) Install the unit in a well-ventilated place with the operating ambient temperature less than 55°C.
- (2) Avoid installing the unit over large sources of heat (such as a large-capacity resistor, transformer, or heater).
- (3) Do not install the unit near sources of vibration or impact loads such as large magnetic contactors and no-fuse breakers.
- (4) Ensure any "L" brackets used do not obstruct the air flow for ventilation.
- (5) When installing the A6FD on a flat surface, ensure that there is sufficient clearance for the connection of solderless terminals to the terminal block.
- (6) The mounting holes in the rear of the face plate are tapped for M4 (Metric thread) screws. Recommended screw length is 5.6mm (0.22inch) plus the panel wall thickness.
- (7) The mounting holes in the rear of the face plate are tapped for M4 (Metric thread) screws. Recommended screw length is mounting surface thickness plus 5mm (0.20inch).

5. LOADING AND INSTALLATION

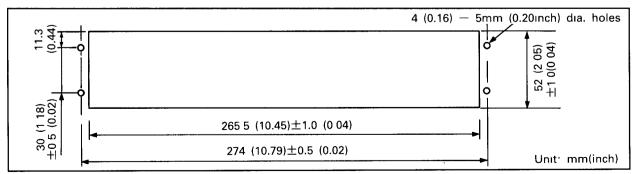


5.2.2 Mounting





(2) Cut-out dimensions and hole centres.

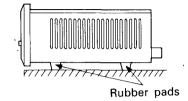


Refer to the rear view in Appendix 1.

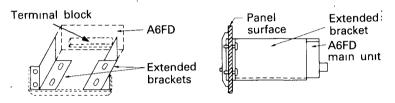


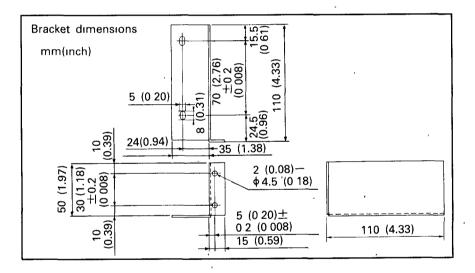
(3) Installation on a flat surface

Rubber pads should be used to provide clearance between the flat surface and the A6FD for the face plate and solderless terminal connections, as shown below.



(4) Installation using extended brackets. See below.





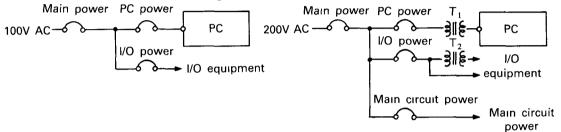


5.3 Wiring

5.3.1 Wiring precautions

(1) Power supply wiring

- (a) When line voltage fluctuations are liable to cause the supply voltage to go outside the specified range, use a constant voltage transformer.
- (b) The power supply used should generate minimal noise between lines and between lines and ground. If the noise generated is excessive, an isolation transformer should be used.
- (c) Separate PC power supply, I/O equipment and main circuit wiring as shown below:



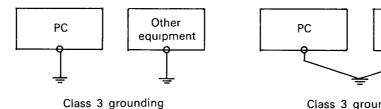
- (d) Use twisted wire for 100V AC, 200V AC and 24V DC wiring and use the shortest wire length possible.
- (e) Use the maximum cable size possible (up to 2mm² (14AWG)) to minimize voltage drops.
- (f) Do not bundle 100V AC and 24V DC wires with the main circuit (high voltage or large current) and I/O signal cables. Avoid running these wires near the main circuit and I/O signal cables and, if possible, keep them more than 100mm (3.94inch) away.
- (2) I/O equipment wiring
 - (a) Use 2mm² (14AWG) wire or smaller for I/O wiring.
 - (b) Run the input wires separately from the output wires.
 - (c) Keep the I/O signal wires more that 100mm (3.94 inch) away from any main circuit wiring.
 - (d) Where I/O signal wires are run in close proximity to other wiring, use shielded cable, preferably grounded at the PC end.
 - (e) Ground any piping used to route cables.
 - (f) Separate the 24V DC I/O signal lines from 100V AC and/or 200V AC wires.
- (3) Grounding wiring
 - (a) Use class 3 grounding (Grounding resistance $100\,\Omega$ or less). Where possible, ground the PC independently of other equipment.
 - (b) Use 2mm² (14AWG) or larger cable for grounding.
 - (c) The grounding point should be as near to the PC as possible.
 - The grounding cable length should be minimal.
 - (d) The unit will still operate if it is not grounded.



Other

equipment

(e) if independent grounding is impractical, use the common grounding method shown below.



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(1) Exclusive grounding. Best

Class 3 grounding

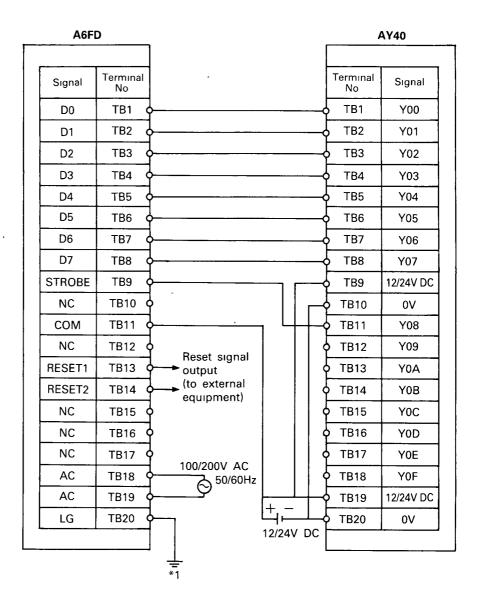
(2) Common grounding. Good

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5.3.2 Output unit wiring

 Connection with sink output unit (AY40, AY41, AY42, AY50, AY51, A0J2-E24T, A0J2-E28DT, A0J2-E56DT)
 Connection with the AY40 output unit is shown below. The I/O numbers used in this example assume that the AY40 is loaded in slot 0 of the main base unit.



*1: The LG terminal must always be grounded.



(2) Connection with source output unit(AY80, AY81) Connection with the AY80 output unit is shown below. The I/O numbers used in this example assume that the AY80 is loaded in slot 0 of the main base unit.

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| A6FC |) | | - | | AY80 |
|---------|--|---|----------|----------------|-----------|
| | `````````````````````````````````````` | | | | |
| Sıgnal | Terminal No | | | Terminal No | Signal |
| D0 | TB1 C |) | പ്പ | TB1 | Y00 |
| D1 | TB2 C |) | ဗု | TB2 | Y01 |
| D2 | TB3 C |) | { | твз | Y02 |
| D3 - | TB4 C | · | { | TB4 | Y03 |
| D4 | TB5 (| , | <u> </u> | TB5 | Y04 |
| · D5 | тв6 с | · | —မ် | TB6 | Y05 |
| . D6 | тв7 с | <u>) </u> | { | TB7 | Y06 |
| D7 | TB8 C | ,, | { | TB8 | Y07 |
| -STROBE | тв9 с | ┝─────┐ [| { | тв9 | 12/24V DC |
| NC | TB10 (| } | гф | TB10 | ٥V |
| СОМ | TB11 (| ┝─────┐└┼ | ┼─┥ | TB11 | Y08 |
| NC | TB12, 0 | | 6 | TB12 | Y09 |
| RESET1 | TB13 (| Reset signal | 6 | TB13 | Y0A |
| RESET2 | TB14 (| (to external | 6 | TB14 | Y0B |
| NC | TB15 (| equipment) | 6 | TB15 | Y0C |
| NC | TB16 0 | ۰ ^۲ | 6 | TB16 | Y0D |
| NC | TB17 (| > . | | TB17 | Y0E |
| AC | TB18 (| 100/200V AC | 6 | TB18 | YOF |
| AC | TB19 | ♀ 50/60Hz + | ┼─∲ | TB19 | 12/24V DC |
| LG | TB20 | , . L | ┥ | тВ20 | ٥V |
| · | · | 12/24V | DC | | |
| | | | | | |

*1: The LG terminal must always be grounded.

5-8



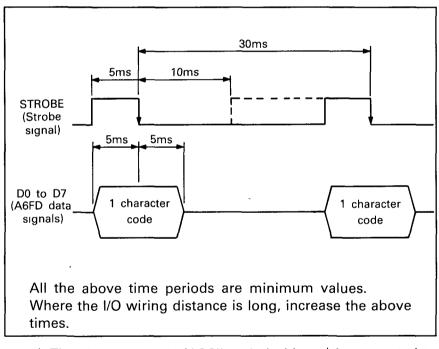
6. DISPLAY CONTROL SIGNALS AND TIMING

The MELSEC-A series range of programmable controllers features two special instructions which may be used to control the A6FD display. These instructions load a series of 16* characters onto the display automatically by controlling the data and strobe lines from the PC operating system. Where other types of message display are required, e.g. a slow scroll, or where a different PC is being used, the sequence program must control the data and strobe lines as explained in this section.

6.1 Display Output Timing

(1) Display control signal timing

The data and strobe signals should be controlled as follows:



- 1) The character code (ASCII code in binary) is presented to the data inputs. After a minimum of 5ms, the strobe signal a switches from high to low.
- The character code is read when the strobe signal switches from high to low, and must be present for a minimum of 5 ms.
- The strobe signal should be on for a minimum of 5ms and off for a minimum of 10ms, however the cycle time should be a minimum of 30ms. (To allow for software processing in the A6FD)
- * When the A3HCPU is used the number of characters is unlimited. In this case consecutive characters are output until code 00H is read from the source data.

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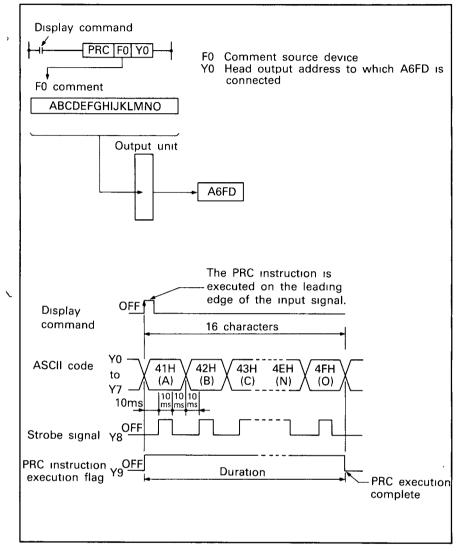


POINT

1. When the A6FD is being used in a remote I/O station (i.e. in the MELSEC-NET datalink network) the PR and PRC instructions cannot be used and the sequence program must control the data and strobe signals.



(2) The PR and PRC instructions generate the following data and strobe signal timings.



- 1) The comment data associated with the source device is displayed via the output module at head address Y0.
- 2) One character is output every 30ms, the total processing time is therefore 30 x 16 = 480ms.
 Processing of the PR and PRC instructions is independent of the PC scan time and does not effect it.
- 3) The strobe signal is generated automatically when the PR and PRC instructions are executed. The strobe signal causes the characters on the display to shift one space to the left.
- 4) The PRC instruction execution flag is switched on to indicate that the character codes are being output. This remains on until all 16 characters have been displayed. It should be used as an interlock in the sequence program to prevent the display instruction from being repeated before all 16 characters have been displayed.

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6.2 Display Method

(1) One character consists of 5 by 7 dots on the LED (red).

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- (2) 16 ASCII characters are displayed from right to left in order. The display may be cleared by pressing the manual reset button. This also initializes the A6FD.
- In the example shown on the previous page, when the display command contact closes the comment associated with device F0 (ABCDEFGHIJKLMNO) is displayed on the A6FD.

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6-4

7. PROGRAMMING



7. PROGRAMMING

7.1 Basic Display Programs

ASCII Print Instructions

- (1) PR instruction
- (2) PRC instruction (device comment)

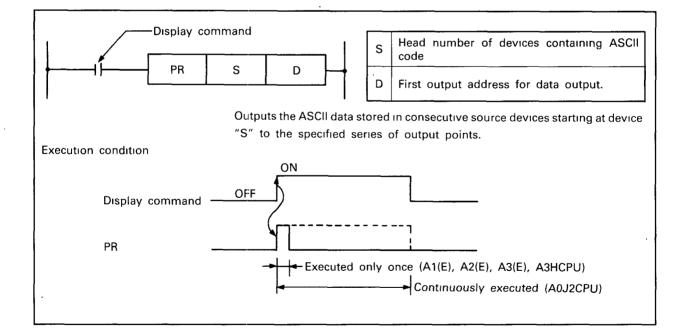
POINT

Only devices marked \circledast are available on the A0J2CPU.

G

(1) PR instruction

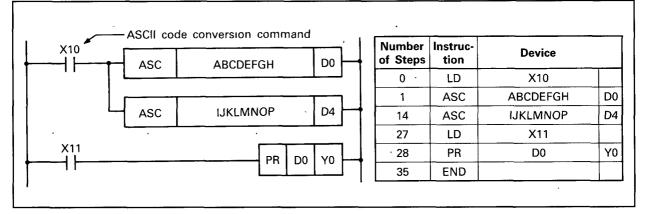
| | Available Device | | | | | | | | | | | | | be- | er ps | ¥ | | Carry | Error | | | | | | | | |
|---|------------------|----|-----|-----|-----|---|---|---|-----|-----|-----|------|------|-----|----------|-----|-------|-------|-------|-------|----------------------------|-----|-----|------|-------|-------|-------|
| | | Bi | t c | lev | ice | | | ۷ | Vor | d (| 16- | bit) | devi | ce | | Con | stant | Poin | ter | Level | it S _l icati | Ste | sdu | vapu | Flag | FI | ag |
| | х | Y | M | L | В | F | Т | С | D | W | R | A0 | A1 | z | V | к | н | Р | I | N | Dig | ς Ζ | ຈັ | | M9012 | M9010 | M9011 |
| s | | | | | | | ۲ | ۲ | • | • | | | | | | | | | | | | - | | 0 | | | |
| D | | ۲ | | | | | | | | | | | | | | | | | | | | / | | | | 0 | 0 |



7. PROGRAMMING

MELSEC-

Basic Program

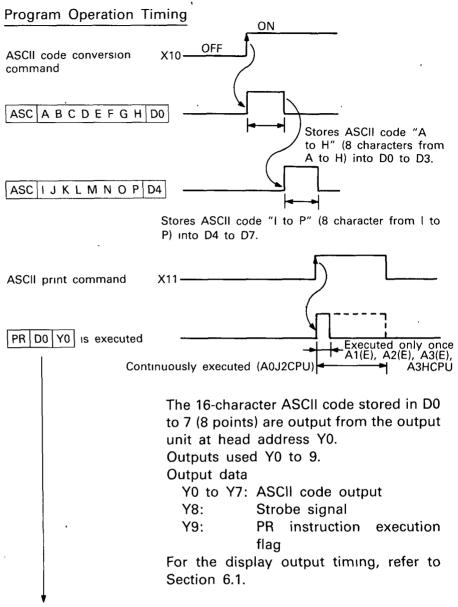


Program Operation

When X10 turns on, "ABCDEFGHIJKLMNOP" is converted into ASCII codes and stored into D0 to D7. When X11 turns on, the ASCII code in D0 to D7 is output to Y0 to Y9.

When the A0J2CPU, the A1(E), A2(E) or A3(E) CPUs are used, 16 consecutive characters are output by this instruction. When the A3HCPU is used the number of output characters is unlimited, the ASCII string is continuously output until the code "00H" is read from the souce data.





The data displayed on the A6FD is shifted from right to left in order until the complete string ABCDEFGHIJKLMNOP is displayed.

POINT

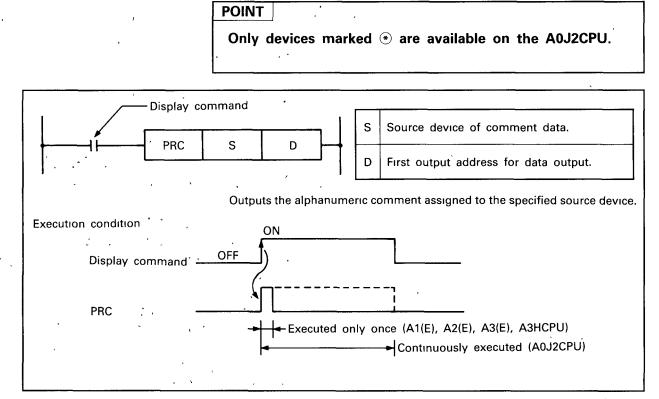
- 1. The ASCII codes used for the PR instruction may be generated using the ASC instruction (alphanumeric character to ASCII code conversion) or may be stored in the PC device memory after using the List Test function to load the alphanumeric data into the source data registers.
- 2. Devices to which the PR instruction is applicable are marked $\bigcirc.$

7. PROGRAMMING

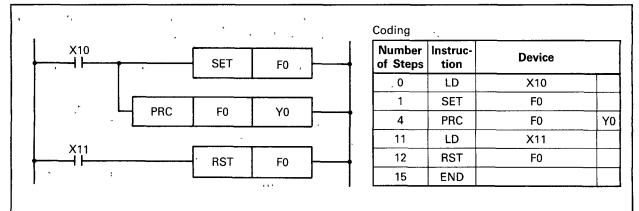


(2) PRC instruction

| $\left(\right)$ | | | | | | | | | | Ava | aila | ble | Devi | ce | | | | | | • | - u ou | er ps | | | Carry | En | ror |
|------------------------|---|----|----|------|-----|---|---|---|-----|------|------|------|------|----|---|-----|-------|------|-----|-------|-----------|----------|------|------|-------|-------|-------|
| $\left \right\rangle$ | | Bi | tc | levi | ice | | | N | /or | d (' | 16- | bit) | devi | ce | | Con | stant | Poin | ter | Level | | Ste | asdi | ndex | Flag | Fla | ag |
| | X | Y | м | L | В | F | т | С | D | w | R | A0 | A1 | z | v | к | н | Р | I | N | Dig | z 2 | งี | - | M9012 | M9010 | M9011 |
| s | 0 | 0 | 0 | 0 | 0 | ۲ | 0 | 0 | 0 | 0 | 0 | | | | | | | 0 | 0 | | | 7 | | 0 | | | |
| D | | ۲ | | | | | | | | | | | | | | | | | | | 1 | · / | | | | | |



Basic Program



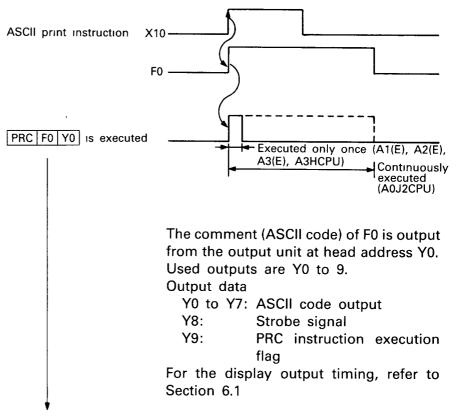
Program Operation

When X10 is turned on, F0 is turned on and, at the same time, the comment of F0(ASCII code) is output to Y0 to Y9.

In this case, the F0 comment ABCDEFGHIJKLMNO has already been entered as comment data.



Program Operation Timing



The data displayed on the A6FD is shifted from right to left in order until the complete string ABCDEFGHIJKLMNOP is displayed

POINT

- 1. Before executing the PRC instruction, comments must be entered into the PC memory comment area.
- Only 128 comments (dedicated to F0 to F127) may be entered on the A1(E)CPU(P21/R21). Only 95 comments (dedicated to F0 to F94) may be entered on the A0J2CPU(P21/R21).
 F coils must be switched on by the OUT or SET
- 3. F coils must be switched on by the OUT or SET instructions only.



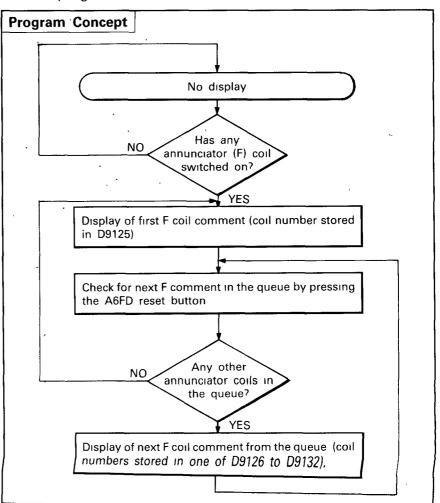
7.2 Programming Application Examples 1 (For A1, A2, A3CPU)

In the following program examples the annunciator devices (F) are uséd to flag error messages. The annunciator devices may be used to queue error codes (in the form of annunciator device numbers) in special function FIFO data registers.

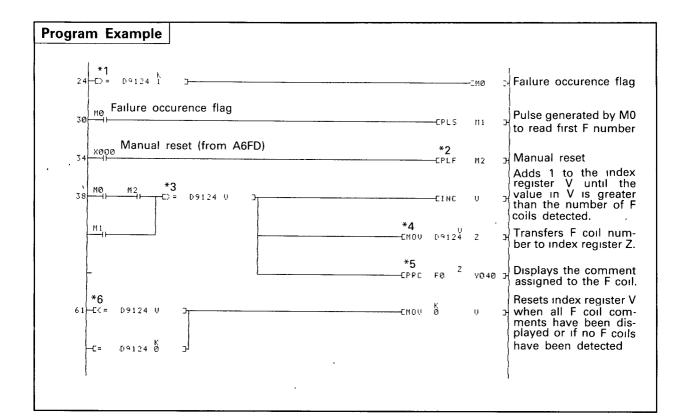
The following special function data registers have been used: D9124(stores the number of F coils which have been switched on, up to a maximum of 8.) and D9125 to D9132 (A sequence of 8 consecutive special registers arranged as a FIFO table in which the annunciator coil numbers are stored as they are switched on). When the first annunciator coil (F) is switched on, D9124 is incremented by 1 and the F coil number is stored in special data register D9125.

Any subsequent annunciator coil switching on which has not already been entered into the queue will cause D9124 to be incremented by 1 and its coil number will be entered into the first empty register after D9125 (up to D9132).

- (1) Display of annunciator comment using manual reset.
 - In this program, the comment for any annunciator coil which has been switched on is output to the display. Pressing the reset button on the A6FD causes the next comment in the queue to be displayed.



The program flow chart is shown below.



| Program | Explanation |
|---------|-------------|
| | anpranacion |

| *1: >= D9124 K1 | Continuity when the number of de- |
|-----------------|---|
| | tected F coil "on" occurences is one or |
| | more. |
| | |

MELSEC-

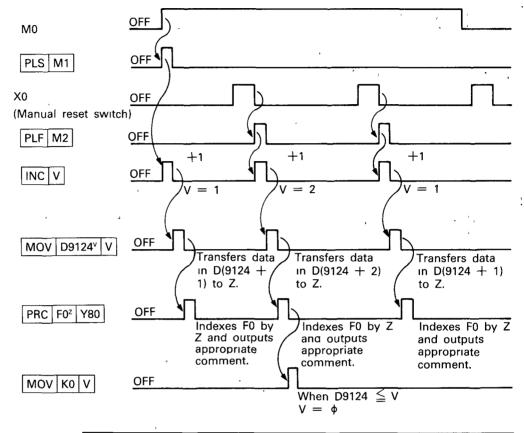
- *2: PLF M2 The reset switch is wired to input X0. When the reset button is released, (i.e. X0 goes from on to off) a pulse is output on M2. This prevents the PRC instruction from being executed while the A6FD is being reset.
- *3: $\geq = |D9124|V|$ Allows 1 to be added to index register V if the value in D9124 is greater than that in V.
- *4: MOV D9124^v Z Moves the contents of data register D9124, indexed by V, (i.e. D(9124 + V)) to index register Z.
- *5: $PRC | F0^2 | Y80 |$ Displays the appropriate F coil comment on the A6FD wired to outputs Y80 ~ Y8F. The F coil number is specified as F0 indexed by Z (i.e. F(0+Z).
- *6: <= D9124 V Checks that all F coil "on" occurences have been processed. If they have, the index register V is reset to 0, and the program will repeat its operation.

7. PROGRAMMING



Program operation

The following explanation assumes that two F coils have been detected (i.e. the value in D9124 is 2).

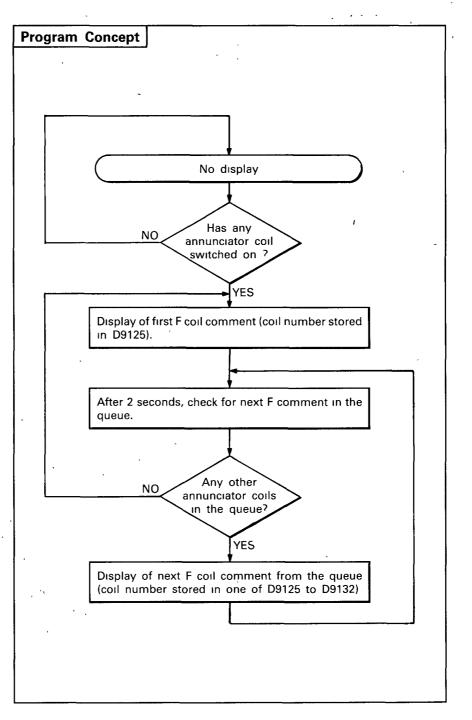


POINT

- 1. Press the manual reset switch to clear the A6FD display.
- Only 8 F coil occurences can be stored in the FIFO table (D9125 to D9132). Removing an entry from the FIFO table causes all subsequent entires to move up one place. When the A3(E)CPU or A3HCPU is used, 1 is subtracted from the contents of D9124 each time the INDICATOR RESET button is pressed. (The INDICATOR RESET button is located on the front of the CPU and is used to reset the CPU's ASCII display).
 Provide interlocks in the PC program to prevent data output to the A6FD before its 200msec start-up time has elapsed.



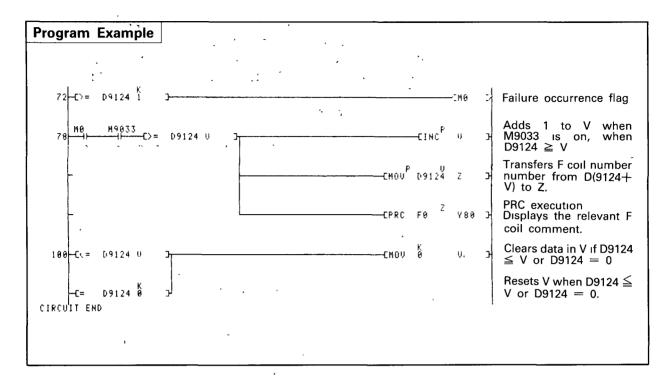
- (2) Display of annunciator comment using PC timer The comment for any annunciator coil which has been switched on is output to the display. Successive F coil entries in the FIFO table are displayed by the internal clock pulse (M9033).
 - The program flow chart is shown below:



7-9

5





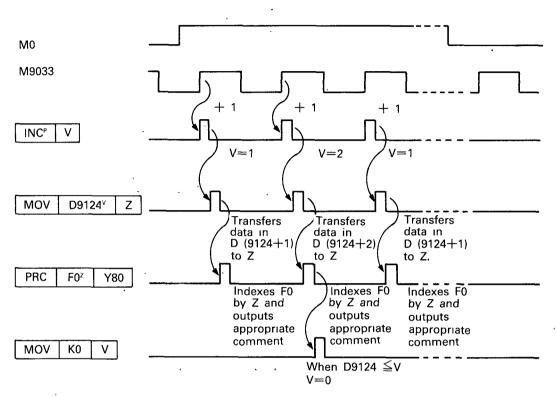
Instruction Explanation

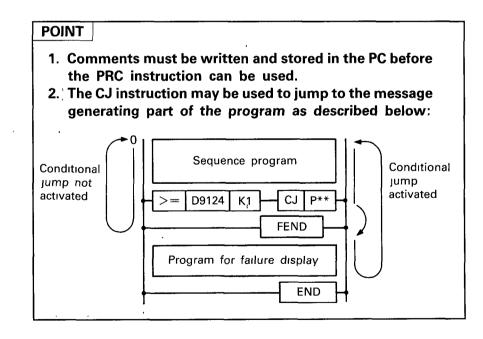
| | Continuity when the number of de- ected F coil "on" occurences is one or |
|--|--|
| · · · · · · · · · · · · · · · · · · · | nore. |
| | pecial function contacts. Close every 1 econd. |
| if | Allows 1 to be added to index register V I the value in D9124 is greater than that In V. |
| *4: MOV [®] D9124 ^v Z M D | Noves the contents of data register 09124, indexed by V, (i.e. D(9124 $+$ V)) o index register Z. |
| *5: PRC F0 ² Y80 D | Displays the appropriate F coil com- nent on the A6FD wired to outputs Y80 ~ Y8F. The F coil number is specified |
| a *6: <= D9124 V C h ir | The Fourier for manipulation of specified is F0 indexed by Z. (i.e. $F(0+Z)$). Checks that all F coil "on" occurences have been processed, if they have, the index register V is reset to 0 and the program will repeat its operation. |



Program operation

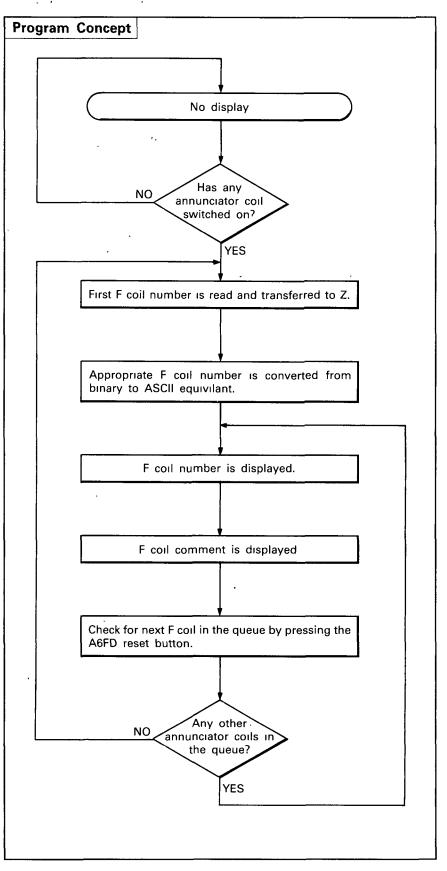
The following explanation assumes that two F coils have been detected (i.e. the value in D9124 is 2).



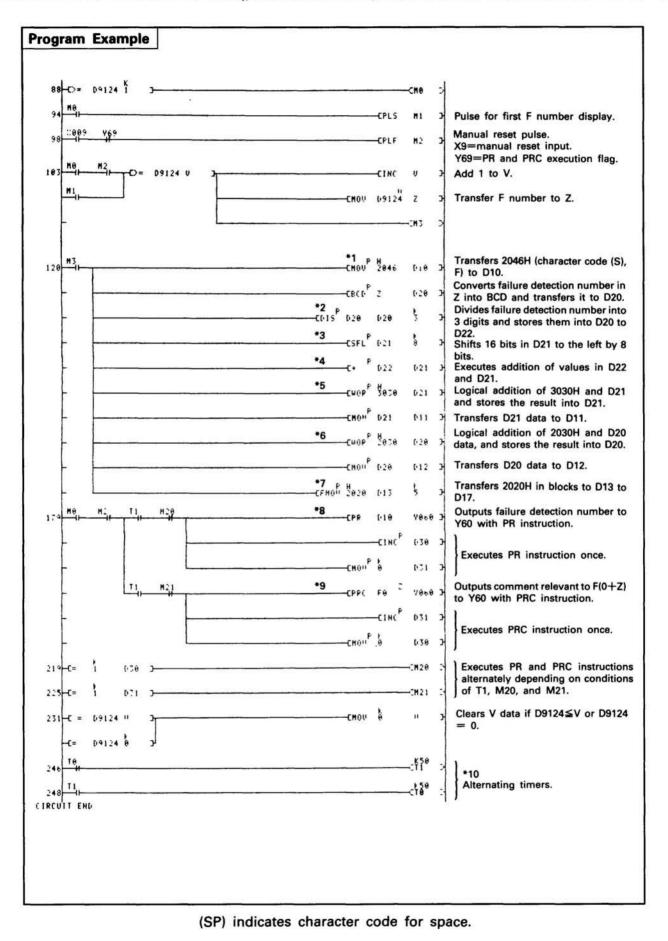




(3) Alternate display of F coil number and comment. This program example outputs a numerical variable (i.e. the F coil number) and a comment alternately.







- 7-13 -



Instruction Explanation

*1: MOV^e #2046 D10 Transfers the character codes for space (H20) and "F" H46 to data

register D10.

*2: DIS[®] D20 D20 K3

*3:|SFL°|D21|K8

D22 D21

.... Takes the three BCD coded digits in D20 and places the least significant in D20 the middle digit in D21 and the most significant in D22. The annunciator coil number digits have therefore been placed in three consecutive registers. (Example: For coil F123 the digits will be stored as follows, 3 in D20, 2 in D21 and 1 in D22.)

... Shifts the value in D21 8 bits to the left (Example: D21 contains the data 0002. This step of the program will change that to 0200.)

...... Adds D22 and D21 and stores the result in D21. (Example: Contents of D21 is 0200, contents of D22 is 0001. This step of the program stores 0201 in D21.)

*5: WOR^e *3030 D21 Logical addition of H30 to each digit in D21 to convert from BCD to ASCII (Example: Contents of D21 is now H3231, i.e. ASCII code for 2 and 1)

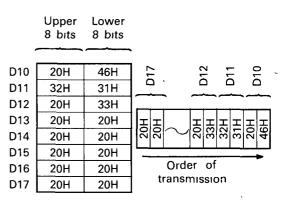
*6: WOR^P H2030 D20 Logical addition of H2030 to digits in D20 (Example: Contents of D20 is now H2033, i.e. ASCII code for space and 3.)

*7: FMOV[®] *2020 D13 K5 ... Transfers space codes H20 into each byte of the five word registers D13 to D17.



*8: PR D10 Y60 Outputs the 16 character codes from D10 to D17 to outputs Y60 to Y67 in A6FD compatible format.

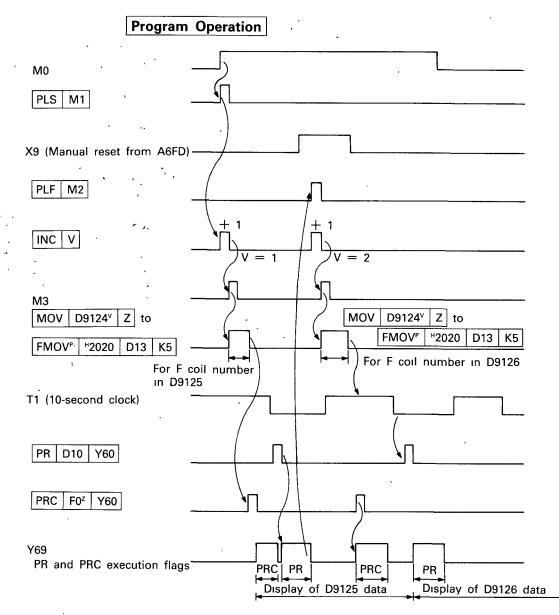
- Character codes sourse



*9: PRC F0^z Y60 Outputs the comment assigned to the appropriate F coil to outputs Y60 to Y67 in A6FD compatible format.

*10: T0, T1 flicker circuit Calls the PR and PRC instructions alternately.



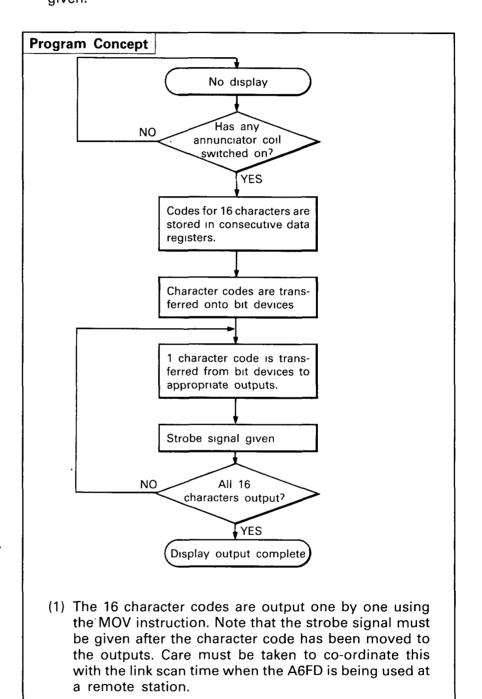


POINT

- 1. When the PR instruction is executed data is transmitted starting from the lower byte of the source data head address.
 - Similarly, the lower byte of the subsequent string of registers is transmitted before the upper byte.
- 2. Interlocks must be provided to prevent the PR and PRC instructions from being called simultaneously.
- 3. Alternating output is provided by T1 (5-second clock). Interlocks are provided using the INC(P) and MOV(P) instructions so that the PR instruction is executed once during the OFF period of T1 and the PRC instruction executed once during the ON period.



(4) A6FD control without using the PR and PRC instructions (Also for use in remote I/O station) The 16 characters for display on the A6FD are stored in eight consecutive data registers and then moved onto a series of internal relays (M). These are moved to the appropriate outputs in batches of 8 bits (one character) and a strobe signal given.



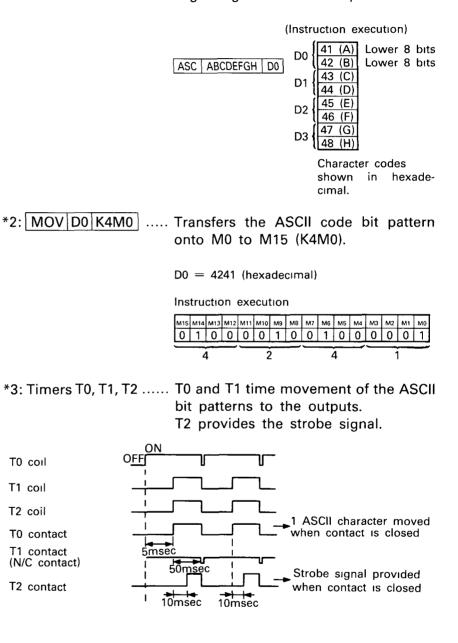


| X009 M128 | | | CPLF M1 | 48 3 | | :-+- |
|-----------|---------------------------------------|-------------------|-----------------|---------------|---|--------|
| M148 | ······ | CASC ABCDEF | 6H D8 | У | Converts 8 characters "A to H" i ASCII code and stores it into D(D3. | |
| - | | | OP D4 | ч | Converts 8 characters "I to P" i ASCII code and stores it into D4 | |
| | | *2 ENOV | De Ké | Э | D7. Transfers ASCII code in D0 t | |
| . | | CMOV | di Ki | 6 3 | D7 registers to M. | 0 |
| . | | CHOV | D2 43 | 2 3 | Data values in registers | |
| . | | CMOV | D3 14 | | | |
| . | | CMOV | | 4 3 | D2 = 4645H D3 = 4847H | |
| . | | | | 0 7 | D4 = 4A49H $D5 = 4C4BH$ | |
| | | | | 6 Э | D6 = 4E4DH D7 = 5D7FH | |
| . | <u> </u> | CMOV | D7 N | 12 3 | • | |
| - | · · · · · · · · · · · · · · · · · · · | | | 128 3 | Sets head number M130 of hit s | ehif |
| | | <u></u> | -CSET M | 130 3 | for 1 character code output. | 5111 |
| N146 T1 | 1 | · | -CRST H | 128 3 | Resets display execution flag. | |
| | | | ласт м | 146 J | Executes reset of final bit after s | shif |
| - | | | | | execution. | |
| M128 T1 | | | CŤ(| k (| High-speed timer T0 = 50 (ms) = 50 (ms), ASCII code output tin | |
| TO | | | K | | flicker circuit | |
| | | | | | | |
| | | | | 2 J 968 J | Strobe signal output timing tin Strobe output (Y68) | ner |
| | | *4 CBSFL | | | | tpu |
| | | | | 2 868 J | | 1 |
| | | _ | | | Upper 8 bits in D0 "42H" | |
| M133 | | CMOVP | | | Lower 8 bits in D1 "43H" | |
| M134 | | | K24 V | 2 060 } | Upper 8 bits in D1 "44H" | |
| H135 | | CHOV ^P | | | Lower 8 bits in D2 "45H" | |
| M136 | | CMOVP | | 2 060 } | | |
| H137 | | CMOV ^P | ₩ 4 8 ¥ | 8 60 3 | Lower 8 bits in D3 "47H" | Y60. |
| H138 | | CMONP | ₩26 V | 2 060] | Upper 8 bits in D3 "48H" | \$ |
| 2 1139 | | CMOV | ₩84 V | 8 60 ∋ | Lower 8 bits in D4 "49H" | output |
| B 140 | | сноч ^р | ₩ 3 2 \$ | 2 660 J | Upper 8 bits in D4 "4AH" | are o |
| M141 | | EMOV ^P | | | Lower 8 bits in D5 "4BH" | |
| H142 | | CM0" | | | Upper 8 bits in D5 "4CH" | |
| 6 H143 | | CMOVP | | | Lower 8 bits in D6 "4DH" | |
| 2 H144 | | CM0VP | | | Upper 8 bits in D6 "4EH" | |
| 8 #145 | | CMOV ^P | | - | Lower 8 bits in D7 "4FH" | |
| 4 H140 | | CMOVP | M120 1 | /060 I | Upper 8 bits in D7 "50H" | l |



Instruction Explanation

*1: ASC ABCDEFGH D0 ... The ASC instruction coverts alphanumeric characters into ASCII code and stores it into four consecutive devices beginning with the one specified.



*4: BSFL[®] M130 K17 ... Shifts 17 bits starting at M130 one bit to the left each time T0 switches on. Used as a 1-character code sending flag.

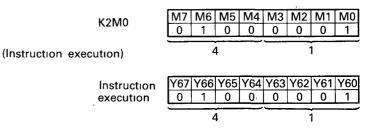
. .



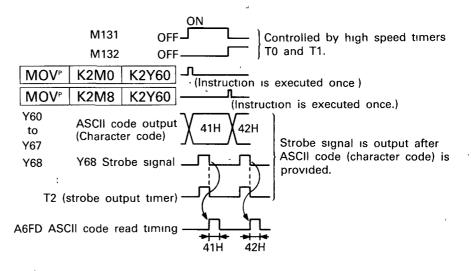
*5: MOV^e K2M0 K2Y60 Outputs one character code of K2M0

("41H" in lower 8 bits of D0) to K2Y60. (Hexadecimal)

Moves one character code from K2M0 to outputs Y60 \sim Y67.



For example, the display output timing for M131 is shown below.





| Program opera | tion |
|---|--|
| X9 | |
| M148 | Π |
| ASC ABCDEFGH D0 | |
| ASC IJKLMNOP D4 | _h |
| MOV D0 K4M0 to | MOV D7 K4M0 to MOV D7 K4M112 |
| MOV D7 K4M112 M128 | ←→ Operation execution complete |
| M120 | |
| T0 coll | |
| T0 contact (50msec) | |
| T1 coil | |
| T1 contact (N/C contact) | |
| (50msec) T2 coil | |
| T2 contact (50msec) Y60 to Y67 (ASCII code output) Y68 (strobe signal) | |
| BSFL [®] M130 K17 | $\frac{1}{2} \frac{1}{3} \frac{1}{4} \frac{1}{5} \frac{1}{6} \frac{1}{7} \frac{1}{8} \frac{1}{9} \frac{1}{10} \frac{1}{11} \frac{1}{12} \frac{1}{13} \frac{1}{14} \frac{1}{15} \frac{1}{16}$ |
| M131 - | |
| M132 | |
| M133 | |
| M134 M135 | |
| M135 M136 | |
| M130 | |
| M138 | |
| M139 | |
| M140 | |
| M141 | |
| M142 | |
| M143 | <u>v</u> tu |
| M144 | |
| M145 | |
| M146 | |

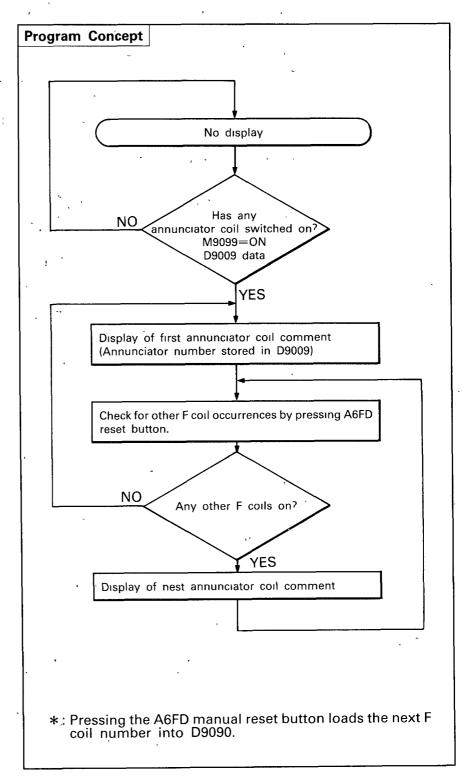
POINT

- 1. The PR and PRC instruction cannot be used to output data from a remote I/O station (owing to the link scan and refresh times). The data output and strobe signals must therefore be controlled from the sequence program.
- 2. The ASC instruction allows alphanumeric character to be converted into character codes. When other characters are to be displayed, use the MOV instruction to transfer the hex. code to the source device.
- 3. Transmit the code for blank space (H20) to character areas which are to remain blank. 16 character codes must therefore always be transmitted even if the message is only 8 characters long for example.
- 4. Only use character codes listed in Appendix 2.



7.3 Programming Application Example 2 (For A0J2CPU)

A6FD control programs are different for the A0J2CPU. The following example displays failure numbers generated when inputs are switched on.





| Prog | ram | Exar | nple | | | | ····· | | | | | | | | |
|---|-----------|-----------|------------------|--------|----------|-----|--|-------------|---------|--------------|-----------|----------|--|--|--|
| I | A | K100 | | | | | | | | | | | | | |
| 0 | 120 | | | | | | | | | CPL S | M 20 | 게 | | | |
| , in the second s | | н 100 | | | | | <u> </u> | | | ESET | FØ | 거 | | | |
| 9 | 121 | | | | | | | ······· | | | M21 | 게 | | | |
| 18 | C 401 | H100 | | | | | | | | CSET | F 1 | 7 | | | |
| | 122 | | | | | | | | | -CPLS | 1122 | Ж | • | | |
| 27 | D | M 100 | | | | | | | | CSET | F 2 | 7 | | | |
| } | 123 | | | | | | | | | CPLS CSET | M23 F3 | Р | | | |
| 36 | Ē | MIQO | | | | | | | | | F 3 | 7 | Ato用respresent failure signal inputs | | |
| 1 | 124 | | | | | | | | | CSET | F4 | ן ר א | | | |
| 4 5 | E | H100 | | | | | | | | -CPLS | 1125 | Э | | | |
| 50 | 125 | | | | | | <u> </u> | | | | F 5 | Э | | | |
| 54 | G | H 100 | | | | | | | | | M26 | Ж | | | |
| 59 | 126 | | | | <u> </u> | | | | | | F6 | Э | | | |
| 63- | ⊞ 1} | N100 | | ······ | | | | | | CPLS | M27 | Я | | | |
| 68 | H27 | | | | | | | ····· | | CSET | F 7 | Э | | | |
| 7 2 | MØ -17 | • | | | | | | | -CMOV | D 900 9 | DØ | н | Stores the first failure number into D9009. | | |
| - | | | • | | | | | - <u></u> | -CMOU | K2 FØ | D 1 | Э | Stores the number of F | | |
| - | Ĺ | | | | | | ······································ | | | | D 1 | Э | coils which have been switched on | | |
| 93- | | | 09003 | 5 | <u></u> | | | <u>-</u> | | CPLS | M 1 Ø Ø | ולכ ו | Resets M9009 as soon | | |
| | 1100 | | | | | | r dotostion | | | | N 908 | 97 | as failure is removed (When failure occurs, | | |
| 105- | {} | | | | ire nu | mbe | r detection | | | CPLS | M11 | Э | M9009 turns on.) | | |
| 165 | 19009 | M11 } | T ^{C =} | K Ø | DØ | J | | | CPRC | FØ | YØ 26 | אנ | | | |
| | | | | | | L | | ····· | | C R S T | FØ | Э | | | |
| - | | | <u></u> -c= | K 1 | 00 | -Tc | | | | F 1 | YØ 20 | ל נ | | | |
| | | | | | | L | | | | CR 5 T | F 1 | Э | | | |
| | | | <u>-</u> c≖ | К 2 | DØ | _ار | | | -CPRC | F 2 | YØ 21 | אל נ | | | |
| | | | | | | L | | | • | CRST | F 2 | Э | Since the PRC instruc- | | |
| | | | -::- | K 3 | DØ | ᅮ | | | -CPRC | F 3 | YØ 21 | דנ | tion in the A0J2 is not leading edge triggered, | | |
| | | | | ., | | L | | | | | FJ | Э | it is necessary to convert external inputs into | | |
| | | | -(= | K 4 | 00 | Ŧ | | | -CPRC | F 4 | YØ 21 | לכ | pulses D0 stores the | | |
| | | | | v | | L | | | | ERST | F4 | Н | current failure number This failure is detected | | |
| - | | | ⊢ נ₌ | К 5 | DØ | 7 | | | C P R C | F 5 | YØ 21 | 6 8 | by the comparison in- struction and the re- | | |
| - | | | | ĸ | | L | | | | ERST | F 5 | Э | levant failure number comment is displayed. If | | |
| | | | ⊢ C = | К 6 | DØ | Т | w | | CPRC | F 6 | | 8 7 | other failures exist, re- | | |
| | | | | к | | L | | | | CR 5 T | F6 | 거 | setting the displayed fai- lure number stores the | | |
| | | | └₋[₌ | К 7 | 00 | Ŧ | | | CPRC | F 7 | | 67 | lowest failure number into D9009 (D0) | | |
| + | | | | | | ۲., | <u>.</u> | | | C R S T | F7 | 거 | 1 | | |



7.4 Display of Numerical Variables

Numerical values may be output using the method described in Section 7.2 (i.e splitting the number into digits and adding H30 to each digit to change it to ASCII).

Where this is impractical the SW0GHP-UTLP-FN1 micro computer software package may be used to change numerical data to the equivilant ASCII string.

8. TEST

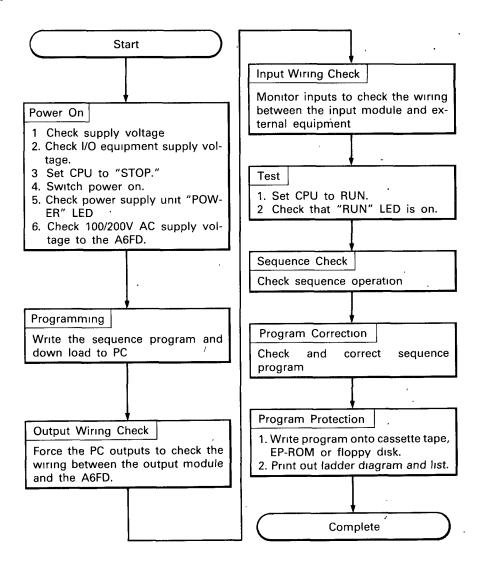
8.1 Pre-Test Checks

Check the following before powering up the A6FD:

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- 1. Installation environment and conditions;
- 2. Output unit type driving the A6FD;
- 3. Output unit correctly loaded in base unit;
- 4. Wiring and connections;
- 5. Wire guages used;
- 6. Power and control line fuses; and
- 7. Supply voltage.

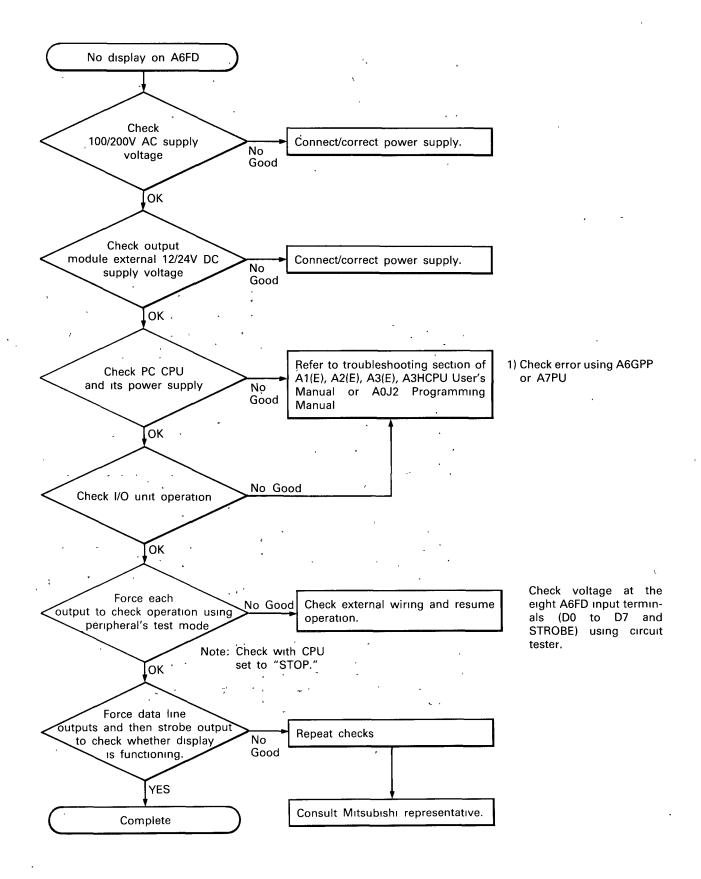
8.2 Test and Adjustment



9. TROUBLESHOOTING



9. TROUBLESHOOTING



9-1

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10. MAINTENANCE AND INSPECTION

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There are no components in the A6FD which require regular service or replacement.

The following periodic checks should be made.

| ltem | Check for | | Corrective action |
|------|------------------------|---|---|
| 1 | Mountings | Looseness, play | Tighten screws |
| • | Mour | Dust and debris | Remove dust and debris |
| | conditions | Loose terminal screws | Tighten screws |
| 2 | | Clearance between solderless terminals | Ensure that there is adequate clearance between solderless terminals. |
| | Connecting | Loose terminal block | Tighten terminal block screws. |
| 3 | L | ine voltage | Line voltage should be between 85 and 264V AC. |
| | t t | Ambient temperature | Must be between 0° and 55°C |
| 4 | Ambient environment | Ambient humidity | Must be between 10% and 95% RH |
| | Ē | Ambience | Must be within the specifications given in section 3.1 |

Fig. 10.1 Check List

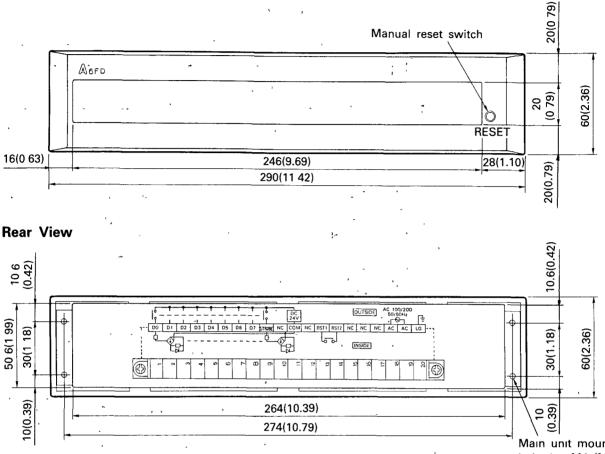
APPENDICES

APPENDIX 1 External Views

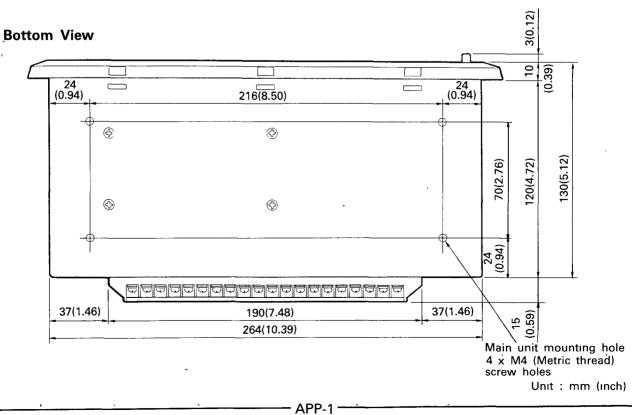


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Front View



Main unit mounting hole 4 x M4 (Metric thread) screw holes







Character Code List

Upper 4 bits 0010 0011 0100 0101 0110 0111 Lower 4 bits (SP) xxxx0000 . . . xxxx0001 **,** • • • xxxx0010 * xxxx0011 , xxxx0100 xxxx0101 ı. ... xxxx0110 i xxxx0111 ٠ xxxx1000 xxxx1001 . • xxxx1010 11 E • :: ':_ xxxx1011 ٩: xxxx1100 28 8 xxxx1101 2 xxxx1110 11 xxxx1111

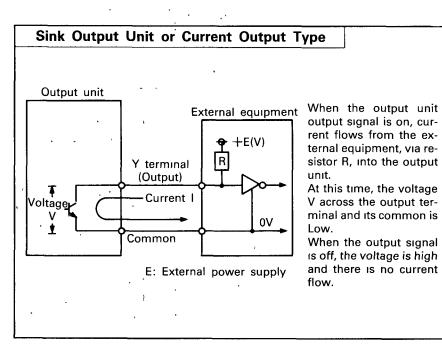
(SP) indicates space.

- APP-2 -

APPENDICES

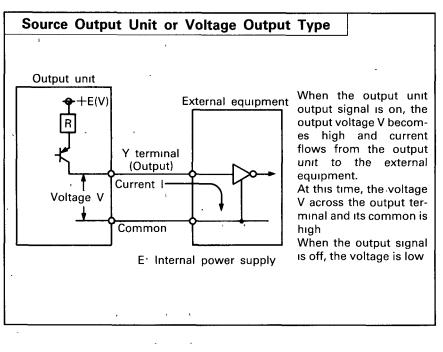
APPENDIX 3

Sink Output and Source Output Units



MELSEC

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- APP-3 -

Index

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Page

| Character code | 6-1, 7-21, APP-2 |
|------------------------------|----------------------|
| Control | 6-1, 6-2, 6-3 |
| Data signal | 6-1 |
| Digital output modules | 2-2, 5-7, 5-8, APP-3 |
| Dimensions | 5-3, APP-1 |
| Display | 6-4 |
| Dot matrix | 6-4 |
| Electrical specifications | 3-3 - |
| Environmental specifications | 3-1, 5-1 |
| Grounding | 3-4, 5-5, 5-6 |
| Handling | 4-1 |
| Inspection | 10-1 |
| Installation | 5-2, 5-3, 5-5 |
| Interface (internal) | 3-3 |
| I/O requirement | 2-2, 3-2, 3-3 |
| LG terminal | 3-4 |
| Maintenance | 10-1 |
| Mounting | 5-2, 5-3 |
| Nomenclature | 4-2 |
| Output modules | 2-2, 5-7, 5-8, APP-3 |
| Power supply | 3-2, 3-4 |
| PR instruction | 6-2, 6-3, 7-1 |
| PRC instruction | 6-2, 6-3, 7-1, 7-4 |
| Pre-test checks | 8-1 |
| Remote I/O station | 6-2, 7-21 |
| Reset push button | 3-3 |
| Reset relay | 3-3 |
| Sink output | APP-3 |
| Source output | APP-3 |
| Specifications | 3-1, 3-2 |
| Strobe signal | 6-1 |
| Supply voltage | 3-2, 3-4 |
| Terminal details | 3-3 |
| Test and adjustment | 8-1 |
| Troubleshooting | 8-2 |
| Voltage, supply | 3-2, 3-4 |
| Wiring | 5-5, 5-7 |
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IMPORTANT

The components on the printed circuit boards will be damaged by static electricity, so avoid handling them directly. If it is necessary to handle them take the following precautions.

- (1) Ground human body and work bench.
- (2) Do not touch the conductive areas of the printed circuit board and its electrical parts with any non-grounded tools etc.

Under no circumstances will Mitsubishi Electric be liable or responsible for any consequential damage that may arise as a result of the installation or use of this equipment

All examples and diagrams shown in this manual are intended only as an aid to understanding the text, not to guarantee operation. Mitsubishi Electric will accept no responsibility for actual use of the product based on these illustrative examples.

Owing to the very great variety in possible applications of this equipment, you must satisfy yourself as to its suitability for your specific application.

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